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Last Name = FARNWORTH
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Application#	Patent#	Status	Date Filed	Title	Inventor Name 50
11108496	Not Issued	020	04/18/2005	GROUP ENCAPSULATED DICING CHUCK	FARNWORTH, WARREN M.
11099066	Not Issued	020	04/05/2005	SYSTEMS AND METHODS OF PLATING VIA INTERCONNECTS	FARNWORTH, WARREN M.
11083057	Not Issued	020	03/16/2005	APPARATUS AND METHODS OF TESTING AND ASSEMBLING BUMPED DEVICES USING AN ANISOTROPICALLY CONDUCTIVE LAYER	FARNWORTH, WARREN M.
11052378	Not Issued	020	02/07/2005	SEMICONDUCTOR COMPONENT AND SYSTEM HAVING THINNED, ENCAPSULATED DICE	FARNWORTH, WARREN M.
11052279	Not Issued	020	02/07/2005	ENCAPSULATED SEMICONDUCTOR COMPONENT HAVING THINNED DIE WITH CONDUCTIVE VIAS	FARNWORTH, WARREN M.
11052181	Not Issued	030	02/07/2005	CONDUCTIVE THROUGH WAFER VIAS	FARNWORTH, WARREN M.
11050100	Not Issued	041	02/03/2005	MODULAR SOCKETS USING FLEXIBLE INTERCONNECTS	FARNWORTH, WARREN M.
11048227	Not Issued	030	02/01/2005	WAFER-LEVEL TESTING APPARATUS	FARNWORTH, WARREN M.
11040555	Not Issued	030	01/21/2005	SEMICONDUCTOR PACKAGE INCLUDING FLEX CIRCUIT, INTERCONNECTS AND DENSE ARRAY EXTERNAL CONTACTS	FARNWORTH, WARREN M.
11031160	Not Issued	030	01/06/2005	SEMICONDUCTOR INTERCONNECT HAVING CONDUCTIVE SPRING	FARNWORTH, WARREN M.

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				CONTACTS	
11030772	Not Issued	030	01/06/2005	METHOD FOR FABRICATING A SEMICONDUCTOR INTERCONNECT HAVING CONDUCTIVE SPRING CONTACTS	FARNWORTH, WARREN M.
11012569	Not Issued	030	12/15/2004	METHODS FOR FORMING PROTECTIVE LAYERS ON SEMICONDUCTOR DEVICE SUBSTRATES	FARNWORTH, WARREN M.
11009829	Not Issued	041	12/10/2004	DEVICE AND METHOD FOR ISOLATING A SHORT-CIRCUITED INTEGRATED CIRCUIT (IC) FROM OTHER ICS ON A SEMICONDUCTOR WAFER	FARNWORTH, WARREN M.
11008654	Not Issued	030	12/09/2004	USE OF PALLADIUM IN IC MANUFACTURING WITH CONDUCTIVE POLYMER BUMP	FARNWORTH, WARREN M.
11002031	Not Issued	041	12/01/2004	METHOD FOR FABRICATING A SEMICONDUCTOR COMPONENT USING CONTACT PRINTING	FARNWORTH, WARREN M.
10998269	Not Issued	030	11/26/2004	TEST SYSTEM WITH INTERCONNECT HAVING CONDUCTIVE MEMBERS AND CONTACTS ON OPPOSING SIDES	FARNWORTH, WARREN M.
10987843	Not Issued	030	11/12/2004	LAYER THICKNESS CONTROL FOR STEREOLITHOGRAPHY UTILIZING VARIABLE LIQUID ELEVATION AND LASER FOCAL LENGTH	FARNWORTH, WARREN M.
10932296	Not Issued	030	09/02/2004	THROUGH-WAFER INTERCONNECTS FOR PHOTOIMAGER AND MEMORY WAFERS	FARNWORTH, WARREN
10847969	Not Issued	094	05/18/2004	METHOD AND APPARATUS FOR MANUFACTURING KNOWN GOOD SEMICONDUCTOR DIE	FARNWORTH, WARREN
10701128	Not Issued	092	11/03/2003	MICROELECTRONIC DEVICE PACKAGES AND METHODS FOR CONTROLLING THE DISPOSITION OF NON-	FARNWORTH, WARREN

				CONDUCTIVE MATERIALS IN SUCH PACKAGES	
10304911	6777261	150	11/26/2002	METHOD AND APPARATUS FOR A SEMICONDUCTOR PACKAGE FOR VERTICAL SURFACE MOUNTING	FARNWORTH, WARREN
10190019	6673649	150	07/05/2002	MICROELECTRONIC DEVICE PACKAGES AND METHODS FOR CONTROLLING THE DISPOSITION OF NON-CONDUCTIVE MATERIALS IN SUCH PACKAGES	FARNWORTH, WARREN
10132429	6828173	150	04/25/2002	SEMICONDUCTOR DEVICE INCLUDING EDGE BOND PADS AND METHODS	FARNWORTH, WARREN M.
10094161	6908784	150	03/06/2002	METHOD FOR FABRICATING ENCAPUSLATED SEMICONDUCTOR COMPONENTS	FARNWORTH, WARREN M.
09949683	6903465	150	09/10/2001	METHOD AND APPARATUS FOR A SEMICONDUCTOR PACKAGE FOR VERTICAL SURFACE MOUNTING	FARNWORTH, WARREN
09749110	6511863	150	12/26/2000	METHOD AND APPARATUS FOR A SEMICONDUCTOR PACKAGE FOR VERTICAL SURFACE MOUNTING	FARNWORTH, WARREN
09651999	6477662	150	08/31/2000	APPARATUS AND METHOD IMPLEMENTING REPAIRS ON A MEMORY DEVICE	FARNWORTH, WARREN M
09388032	6082605	150	09/01/1999	CONTINUOUS MODE SOLDER JET APPARATUS	FARNWORTH, WARREN M
09302833	6204678	150	04/30/1999	DIRECT CONNECT INTERCONNECT FOR TESTING SEMICONDUCTOR DICE AND WAFERS	FARNWORTH, WARREN M
09275791	6437591	150	03/25/1999	TEST INTERCONNECT FOR BUMPED SEMICONDUCTOR COMPONENTS AND METHOD OF FABRICATION	FARNWORTH, WARREN M
09266237	Not Issued	094	03/10/1999	TEST INTERCONNECT FOR BUMPED SEMICONDUCTOR COMPONENTS AND METHOD OF FABRICATION	FARNWORTH, WARREN M.
09259142	6549821	150	02/26/1999	STEREOLITHOGRAPHIC METHOD AND APPARATUS	FARNWORTH, WARREN M

				FOR PACKAGING ELECTRONIC COMPONENTS AND RESULTING STRUCTURES	
09244733	6084288	150	02/05/1999	HERMETIC CHIP AND METHOD OF MANUFACTURE	FARNWORTH, WARREN M
09143765	6291894	150	08/31/1998	METHOD AND APPARATUS FOR A SEMICONDUCTOR PACKAGE FOR VERTICAL SURFACE MOUNTING	FARNWORTH, WARREN
08975553	6219908	150	11/20/1997	METHOD AND APPARATUS FOR MANUFACTURING KNOWN GOOD SEMICONDUCTOR DIE	FARNWORTH, WARREN
08975549	6763578	150	11/20/1997	METHOD AND APPARATUS FOR MANUFACTURING KNOWN GOOD SEMICONDUCTOR DIE	FARNWORTH, WARREN
08962229	6124721	150	10/31/1997	METHOD OF TESTING THE OOPERABILITY OF INTEGRATED CIRCUITRY OF A SUBSTRATE BY ENGAGING ELECTRICALLY CONDUCTIVE TEST PADS ON THE SUBSTRATE	FARNWORTH, WARREN
08838010	5910921	150	04/22/1997	SELF-TEST OF A MEMORY DEVICE	FARNWORTH, WARREN M
08823468	5796264	150	03/19/1997	APPARATUS FOR MANUFACTURING KNOWN GOOD SEMICONDUCTOR DICE	FARNWORTH, WARREN
08821468	6025731	150	03/21/1997	HYBRID INTERCONNECT AND SYSTEM FOR TESTING SEMICONDUCTOR DICE	FARNWORTH, WARREN M
08758657	Not Issued	161	12/02/1996	METHOD AND APPARATUS FOR MANUFACTURING KNOWN GOOD SEMICONDUCTOR DIE	FARNWORTH, WARREN
08692883	5958100	150	07/31/1996	PROCESS OF MAKING A GLASS SEMICONDUCTOR PACKAGE	FARNWORTH, WARREN
08691759	6005290	150	08/02/1996	MULTI CHIP MODULE HAVING SELF LIMITING CONTACT MEMBERS	FARNWORTH, WARREN
08621157	Not	161	03/21/1996	METHOD AND APPARATUS	FARNWORTH,

	Issued			FOR TESTING SEMICONDUCTOR CIRCUITRY FOR OPERABILITY AND METHOD OF FORMING APPARATUS FOR TESTING SEMICONDUCTOR CIRCUITRY FOR OPERABILITY	WARREN
08485086	5640762	150	06/07/1995	METHOD AND APPARATUS FOR MANUFACTURING KNOWN GOOD SEMICONDUCTOR DIE	FARNWORTH, WARREN
08452990	5559444	150	05/30/1995	METHOD AND APPARATUS FOR TESTING UNPACKAGED SEMICONDUCTOR DICE	FARNWORTH, WARREN
08452798	5607818	150	05/30/1995	MAKING INTERCONNECTS AND SEMICONDUCTOR STRUCTURES USING ELECTROPHORETIC PHOTORESIST DEPOSITION	FARNWORTH, WARREN
08431452	5578526	150	05/01/1995	METHOD FOR FORMING A MULTI CHIP MODULE (MCM)	FARNWORTH, WARREN
08343730	5487999	150	11/22/1994	METHOD FOR FABRICATING A PENETRATION LIMITED CONTACT HAVING A ROUGH TEXTURED SURFACE	FARNWORTH, WARREN M
08338345	5634267	150	11/14/1994	METHOD AND APPARATUS FOR MANUFACTURING KNOWN GOOD SEMICONDUCTOR DIE	FARNWORTH, WARREN

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Application#	Patent#	Status	Date Filed	Title	Inventor Name 50
10443347	6781848	150	05/22/2003	SINGLE-PIECE MOLDED MODULE HOUSING	FARNWORTH, WARREN M.
10430616	Not Issued	094	05/05/2003	USE OF PALLADIUM IN IC MANUFACTURING WITH CONDUCTIVE POLYMER BUMP	FARNWORTH, WARREN M.
10427518	Not Issued	120	05/01/2003	METHODS FOR MODIFYING A VERTICAL SURFACE MOUNT PACKAGE	FARNWORTH, WARREN M.
10423240	6815327	150	04/25/2003	MASK REPATTERN PROCESS	FARNWORTH, WARREN M.
10401209	Not Issued	092	03/27/2003	METHOD FOR USING DATA REGARDING MANUFACTURING PROCEDURES INTEGRATED CIRCUITS (IC'S) HAVE UNDERGONE, SUCH AS REPAIRS, TO SELECT PROCEDURES THE IC'S UNDERGO, SUCH AS ADDITIONAL REPAIRS	FARNWORTH, WARREN M.
10401199	Not Issued	094	03/27/2003	APPARATUS FOR FORMING MODULAR SOCKETS USING FLEXIBLE INTERCONNECTS AND RESULTING STRUCTURES	FARNWORTH, WARREN M.
10396163	6801048	150	03/25/2003	DEVICE AND METHOD FOR TESTING INTEGRATED CIRCUIT DICE IN AN INTEGRATED CIRCUIT MODULE	FARNWORTH, WARREN M.
10391067	6852999	150	03/17/2003	REDUCED TERMINAL TESTING SYSTEM	FARNWORTH, WARREN M.
10390863	6906418	150	03/17/2003	SEMICONDUCTOR	FARNWORTH,

				COMPONENT HAVING ENCAPSULATED, BONDED, INTERCONNECT CONTACTS	WARREN M.
10370755	Not Issued	041	02/20/2003	CHIP SCALE PACKAGE STRUCTURES AND METHOD OF FORMING CONDUCTIVE BUMPS THEREON	FARNWORTH, WARREN M.
10359778	6662243	150	02/07/2003	MEMORY UNIT, A METHOD OF ASSEMBLING A MEMORY UNIT, A METHOD OF RECONFIGURING A SYSTEM, AND A MEMORY DEVICE	FARNWORTH, WARREN M.
10357656	6787394	150	02/03/2003	METHODS OF WAFER LEVEL FABRICATION AND ASSEMBLY OF CHIP SCALE PACKAGES	FARNWORTH, WARREN M.
10357587	6768209	150	02/03/2003	UNDERFILL COMPOUNDS INCLUDING ELECTRICALLY CHARGED FILLER ELEMENTS, MICROELECTRONIC DEVICES HAVING UNDERFILL COMPOUNDS INCLUDING ELECTRICALLY CHARGED FILLER ELEMENTS, AND METHODS OF UNDERFILLING MICROELECTRONIC DEVICES	FARNWORTH, WARREN M.
10342798	Not Issued	094	01/13/2003	THE USE OF NITRIDES FOR FLIP-CHIP ENCAPSULATION	FARNWORTH, WARREN M.
10341451	6713788	150	01/14/2003	OPTO-ELECTRIC MOUNTING APPARATUS	FARNWORTH, WARREN M.
10193567	6803303	150	07/11/2002	METHOD OF FABRICATING SEMICONDUCTOR COMPONENT HAVING ENCAPSULATED, BONDED, INTERCONNECT CONTACTS	FARNWORTH, WARREN M.
10193001	6767817	150	07/11/2002	ASYMMETRIC PLATING	FARNWORTH, WARREN M.
10191424	Not Issued	094	07/08/2002	SEMICONDUCTOR DEVICES WITH PERMANENT POLYMER STENCIL AND METHOD FOR MANUFACTURING THE SAME	FARNWORTH, WARREN M.
10107884	6600171	150	03/28/2002	SEMICONDUCTOR COMPONENT AND SYSTEM FOR FABRICATING CONTACTS ON SEMICONDUCTOR COMPONENTS	FARNWORTH, WARREN M.
10099483	6684493	150	03/13/2002	METHOD OF CONNECTING	FARNWORTH,

				SEMICONDUCTOR DEVICE TO A SUBSTRATE	WARREN M.
10092829	6515325	150	03/06/2002	NANOTUBE SEMICONDUCTOR DEVICES AND METHODS FOR MAKING THE SAME	FARNWORTH, WARREN M.
10074844	6514798	150	02/13/2002	STEREOLITHOGRAPHIC METHODS FOR FABRICATING HERMETIC SEMICONDUCTOR DEVICE PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING STEREOLITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES	FARNWORTH, WARREN M.
10074840	6593171	150	02/13/2002	STEREOLITHOGRAPHIC METHODS FOR FABRICATING HERMETIC SEMICONDUCTOR DEVICE PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING STEREOLITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES	FARNWORTH, WARREN M.
10072734	6798224	150	02/05/2002	METHOD FOR TESTING SEMICONDUCTOR WAFERS	FARNWORTH, WARREN M.
10035078	Not Issued	093	12/28/2001	LOC SEMICONDUCTOR ASSEMBLED WITH ROOM TEMPRATURE ADHESIVE	FARNWORTH, WARREN M.
10017419	6555460	150	12/12/2001	METHODS FOR MASK REPATTERN PROCESS	FARNWORTH, WARREN M.
09921205	6426562	150	08/02/2001	MASK REPATTERN PROCESS	FARNWORTH, WARREN M.
09917130	6541850	150	07/27/2001	UTILIZATION OF DIE ACTIVE SURFACES FOR LATERALLY EXTENDING DIE INTERNAL AND EXTERNAL CONNECTIONS	FARNWORTH, WARREN M.
09906294	6469537	150	07/16/2001	SYSTEM FOR TESTING SEMICONDUCTOR WAFERS HAVING INTERCONNECT WITH PRESSURE SENSING MECHANISM	FARNWORTH, WARREN M.
09892156	6420681	150	06/26/2001	METHOD AND PROCESS OF CONTACT TO A HEAT SOFTENED SOLDER BALL ARRAY	FARNWORTH, WARREN M.

09885325	6426639	150	06/19/2001	METHOD AND APPARATUS FOR CAPACITIVELY TESTING A SEMICONDUCTOR DIE	FARNWORTH, WARREN M.
09875063	Not Issued	061	06/06/2001	GROUP ENCAPSULATED DICING CHUCK	FARNWORTH, WARREN M.
09867374	6395566	150	05/29/2001	METHOD OF WIRE-BONDING A REPAIR DIE IN A MULTI-CHIP MODULE USING A REPAIR SOLUTION GENERATED DURING TESTING OF THE MODULE	FARNWORTH, WARREN M.
09844532	Not Issued	061	04/30/2001	METHOD FOR FABRICATING A TEST INTERCONNECT FOR BUMPED SEMICONDUCTOR COMPONENTS BY FORMING RECESSES AND CANTILEVERED LEADS ON A SUBSTRATE	FARNWORTH, WARREN M.
09808879	6573740	150	03/14/2001	METHOD OF FORMING AN APPARATUS CONFIGURED TO ENGAGE AN ELECTRICALLY CONDUCTIVE PAD ON A SEMICONDUCTIVE SUBSTRATE AND A METHOD OF ENGAGING ELECTRICALLY CONDUCTIVE PADS ON A SEMICONDUCTIVE SUBSTRATE	FARNWORTH, WARREN M.
09797743	6356092	150	03/02/2001	METHOD AND APPARATUS FOR CAPACITIVELY TESTING A SEMICONDUCTOR DIE	FARNWORTH, WARREN M.
09754671	6365501	150	01/04/2001	MASK REPATTERN PROCESS	FARNWORTH, WARREN M.
09734383	6379983	150	12/11/2000	METHOD OF WIRE-BONDING A REPAIR DIE IN A MULTI-CHIP MODULE USING A REPAIR SOLUTION GENERATED DURING TESTING OF THE MODULE	FARNWORTH, WARREN M.
09733572	6509647	150	12/08/2000	APPARATUS AND METHODS OF TESTING AND ASSEMBLING BUMPED DEVICES USING AN ANISOTROPICALLY CONDUCTIVE LAYER	FARNWORTH, WARREN M.
09723101	6396291	150	11/28/2000	METHOD FOR TESTING SEMICONDUCTOR COMPONENTS	FARNWORTH, WARREN M.
09660782	6410406	150	09/13/2000	SEMICONDUCTOR DEVICE	FARNWORTH,

				INCLUDING EDGE BOND PADS AND METHODS	WARREN M.
09534822	6686758	150	03/23/2000	ENGAGEMENT PROBE AND APPARATUSES CONFIGURED TO ENGAGE A CONDUCTIVE PAD	FARNWORTH, WARREN M.
09533407	6529027	150	03/23/2000	INTERPOSER AND METHODS FOR FABRICATING SAME	FARNWORTH, WARREN M.
09525017	6514787	150	03/14/2000	OPTO-ELECTRIC MOUNTING APPARATUS	FARNWORTH, WARREN M.
09524800	6705513	150	03/14/2000	METHODS OF BONDING SOLDER BALLS TO BOND PADS ON A SUBSTRATE, AND BONDING FRAMES	FARNWORTH, WARREN M.
09513274	Not Issued	160	02/24/2000	METHOD OF STRESS TESTING MEMORY INTEGRATED CIRCUITS	FARNWORTH, WARREN M.
09505214	6228677	150	02/16/2000	VERTICAL SURFACE MOUNT ASSEMBLY AND METHODS	FARNWORTH, WARREN M.
09499768	6362642	150	02/08/2000	METHOD OF CHIP TESTING OF CHIP LEADS CONSTRAINED IN DIELECTRIC MEDIA	FARNWORTH, WARREN M.
09482416	6453550	150	01/13/2000	METHOD AND APPARATUS FOR FORMING MODULAR SOCKETS USING FLEXIBLE INTERCONNECTS AND RESULTING STRUCTURES	FARNWORTH, WARREN M.
09479894	6407570	150	01/18/2000	INTERCONNECT FOR TESTING SEMICONDUCTOR COMPONENT FLEXURE	FARNWORTH, WARREN M.

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Application#	Patent#	Status	Date Filed	Title	Inventor Name 50
09819297	6455351	150	03/27/2001	VERTICAL SURFACE MOUNT ASSEMBLY AND METHODS	FARNWORTH, WARREN M.
09797795	6369597	150	03/02/2001	METHOD AND APPARATUS FOR CAPACITIVELY TESTING A SEMICONDUCTOR DIE	FARNWORTH, WARREN M.
09797368	6605956	150	03/01/2001	DEVICE AND METHOD FOR TESTING INTEGRATED CIRCUIT DICE IN AN INTEGRATED CIRCUIT MODULE	FARNWORTH, WARREN M.
09795889	6391680	150	02/28/2001	LOC SEMICONDUCTOR ASSEMBLED WITH ROOM TEMPERATURE ADHESIVE	FARNWORTH, WARREN M.
09769983	6407451	150	01/25/2001	MICROMACHINED CHIP SCALE PACKAGE	FARNWORTH, WARREN M.
09749173	6400174	150	12/27/2000	TEST SYSTEM HAVING ALIGNMENT MEMBER FOR ALIGNING SEMICONDUCTOR COMPONENTS	FARNWORTH, WARREN M.
09745093	6353328	150	12/20/2000	TEST SYSTEM WITH MECHANICAL ALIGNMENT FOR SEMICONDUCTOR CHIP SCALE PACKAGES AND DICE	FARNWORTH, WARREN M.
09738915	6437451	150	12/14/2000	TEST INTERCONNECT FOR SEMICONDUCTOR COMPONENTS HAVING BUMPED AND PLANAR CONTACTS	FARNWORTH, WARREN M.
09737288	6570771	150	12/13/2000	SINGLE-PIECE MOLDED MODULE HOUSING	FARNWORTH, WARREN M.
09734514	6297660	150	12/12/2000	TEST CARRIER WITH VARIABLE FORCE APPLYING MECHANISM FOR TESTING SEMICONDUCTOR	FARNWORTH, WARREN M.

				COMPONENTS	
09732106	6442044	150	12/07/2000	SEMICONDUCTOR DEVICE SOCKET, ASSEMBLY AND METHODS	FARNWORTH, WARREN M.
09693443	6319354	150	10/20/2000	SYSTEM AND METHOD FOR DICING SEMICONDUCTOR COMPONENTS	FARNWORTH, WARREN M.
09685132	6420892	150	10/10/2000	CALIBRATION TARGET FOR CALIBRATING SEMICONDUCTOR WAFER TEST SYSTEMS	FARNWORTH, WARREN M.
09677555	6544461	150	10/02/2000	TEST CARRIER WITH MOLDED INTERCONNECT FOR TESTING SEMICONDUCTOR COMPONENTS	FARNWORTH, WARREN M.
09653366	6756253	150	09/01/2000	METHOD FOR FABRICATING A SEMICONDUCTOR COMPONENT WITH EXTERNAL CONTACT POLYMER SUPPORT LAYER	FARNWORTH, WARREN M.
09653272	Not Issued	061	08/31/2000	METHOD FOR USING DATA REGARDING MANUFACTURING PROCEDURES INTEGRATED CIRCUITS (IC'S) HAVE UNDERGONE, SUCH AS REPAIRS, TO SELECT PROCEDURES THE IC'S WILL UNDERGO, SUCH AS ADDITIONAL REPAIRS	FARNWORTH, WARREN M.
09652923	6757972	150	08/31/2000	ELECTRICAL CONNECTOR	FARNWORTH, WARREN M.
09652922	6397460	150	08/31/2000	ELECTRICAL CONNECTOR	FARNWORTH, WARREN M.
09652724	6729027	150	08/31/2000	METHOD OF FORMING RECESSED SOCKET CONTACTS	FARNWORTH, WARREN M.
09652720	6648654	150	08/31/2000	ELECTRICAL CONNECTOR	FARNWORTH, WARREN M.
09652585	Not Issued	093	08/31/2000	PROCESS OF FORMING SOCKET CONTACTS	FARNWORTH, WARREN M.
09652340	6544902	150	08/31/2000	ENERGY BEAM PATTERNING OF PROTECTIVE LAYERS FOR SEMICONDUCTOR DEVICES	FARNWORTH, WARREN M.
09652060	6656754	150	08/31/2000	METHOD OF FORMING A SEMICONDUCTOR CHIP	FARNWORTH, WARREN M.

				CARRIER	
09651863	6562278	150	08/29/2000	STEREOLITHOGRAPHIC FABRICATION OF MONOLITHIC HOUSING STRUCTURES FOR MACHINES	FARNWORTH, WARREN M.
09651860	6628128	150	08/30/2000	CSP BGA TEST SOCKET WITH INSERT AND METHOD	FARNWORTH, WARREN M.
09651601	6358833	150	08/30/2000	METHOD OF FABRICATING A MICROMACHINED CHIP SCALE PACKAGE	FARNWORTH, WARREN M.
09651491	6437435	150	08/30/2000	VERTICALLY MOUNTABLE INTERPOSER, ASSEMBLY AND METHOD	FARNWORTH, WARREN M.
09651490	6492187	150	08/30/2000	METHOD AND APPARATUS FOR AUTOMATICALLY POSITIONING ELECTRONIC DICE WITHIN COMPONENT PACKAGES	FARNWORTH, WARREN M.
09651460	6295209	150	08/30/2000	SEMICONDUCTOR DEVICE INCLUDING COMBED BOND PAD OPENING, ASSEMBLIES AND METHODS	FARNWORTH, WARREN M.
09651175	6607689	150	08/29/2000	LAYER THICKNESS CONTROL FOR STEREOLITHOGRAPHY UTILIZING VARIABLE LIQUID ELEVATION AND LASER FOCAL LENGTH	FARNWORTH, WARREN M.
09651040	6632343	150	08/30/2000	METHOD AND APPARATUS FOR ELECTROLYTIC PLATING OF SUREFACE METALS	FARNWORTH, WARREN M.
09645947	6413862	150	08/25/2000	USE OF PALLADIUM IN IC MANUFACTURING	FARNWORTH, WARREN M.
09644248	6614249	150	08/22/2000	METHOD AND APPARATUS FOR TESTING SEMICONDUCTOR CIRCUITRY FOR OPERABILITY AND METHOD OF FORMING APPARATUS FOR TESTING SEMICONDUCTOR CIRCUITRY FOR OPERABILITY	FARNWORTH, WARREN M.
09642133	6544465	150	08/18/2000	METHOD FOR FORMING THREE DIMENSIONAL STRUCTURES FROM LIQUID WITH IMPROVED SURFACE FINISH	FARNWORTH, WARREN M.
09640801	6432752	150	08/17/2000	STEREOLITHOGRAPHIC METHODS FOR FABRICATING	FARNWORTH, WARREN M.

				HERMETIC SEMICONDUCTOR DEVICE PACKAGES AND SEMICONDUCTOR DEVICES INCLUDING STEREOLITHOGRAPHICALLY FABRICATED HERMETIC PACKAGES	
09639422	6597066	150	08/14/2000	HERMETIC CHIP AND METHOD OF MANUFACTURE	FARNWORTH, WARREN M.
09638276	6534785	150	08/14/2000	REDUCED TERMINAL TESTING SYSTEM	FARNWORTH, WARREN M.
09634239	6482576	150	08/08/2000	SURFACE SMOOTHING OF STEREOLITHOGRAPHICALLY FORMED 3-D OBJECTS	FARNWORTH, WARREN M.
09633915	6537482	150	08/08/2000	UNDERFILL AND ENCAPSULATION OF CARRIER SUBSTRATE-MOUNTED FLIP-CHIP COMPONENTS USING STEREOLITHOGRAPHY	FARNWORTH, WARREN M.
09631903	6562637	150	08/04/2000	APPARATUS AND METHODS OF TESTING AND ASSEMBLING BUMPED DEVICES USING AN ANISOTROPICALLY CONDUCTIVE LAYER	FARNWORTH, WARREN M.
09631900	6791185	150	08/04/2000	APPARATUS AND METHODS OF TESTING AND ASSEMBLING BUMPED DEVICES USING AN ANISOTROPICALLY CONDUCTIVE LAYER	FARNWORTH, WARREN M.
09618885	6329637	150	07/18/2000	METHOD AND PROCESS OF CONTACT TO A HEAT SOFTENED SOLDER BALL ARRAY	FARNWORTH, WARREN M.
09606428	6576494	150	06/28/2000	RECESSED ENCAPSULATED MICROELECTRONIC DEVICES AND METHODS FOR FORMATION	FARNWORTH, WARREN M.
09596272	6600334	150	06/16/2000	FORCE APPLYING PROBE CARD AND TEST SYSTEM FOR SEMICONDUCTOR WAFERS	FARNWORTH, WARREN M.
09589841	6875640	150	06/08/2000	STEREOLITHOGRAPHIC METHODS FOR FORMING A PROTECTIVE LAYER ON A SEMICONDUCTOR DEVICE SUBSTRATE AND SUBSTRATES INCLUDING PROTECTIVE LAYERS SO FORMED	FARNWORTH, WARREN M.

09580814	6265245	150	05/26/2000	COMPLIANT INTERCONNECT FOR TESTING A SEMICONDUCTOR DIE	FARNWORTH, WARREN M.
09536827	6465877	150	03/27/2000	SEMICONDUCTOR PACKAGE INCLUDING FLEX CIRCUIT, INTERCONNECTS AND DENSE ARRAY EXTERNAL CONTACTS	FARNWORTH, WARREN M.
09518293	6287942	150	03/03/2000	HERMETIC CHIP AND METHOD OF MANUFACTURE	FARNWORTH, WARREN M.
09505493	6215183	150	02/16/2000	VERTICAL SURFACE MOUNT ASSEMBLY AND METHODS	FARNWORTH, WARREN M.
09487935	6319065	150	01/20/2000	METHOD AND APPARATUS FOR FORMING MODULAR SOCKETS USING FLEXIBLE INTERCONNECTS AND RESULTING STRUCTURES	FARNWORTH, WARREN M.

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Application#	Patent#	Status	Date Filed	Title	Inventor Name 50
60606798	Not Issued	020	09/01/2004	METHODS AND APPARATUS FOR PLACING SUBSTRATES IN CONTACT WITH MOLTEN SOLDER	KIRBY, KYLE K.
60606355	Not Issued	020	08/31/2004	THROUGH-SUBSTRATE INTERCONNECT FABRICATION METHODS AND RESULTING STRUCTURES AND ASSEMBLIES	KIRBY, KYLE K.
60604234	Not Issued	020	08/24/2004	METHOD OF FORMING VIAS IN SEMICONDUCTOR SUBSTRATES WITHOUT DAMAGING ACTIVE REGIONS THEREOF AND RESULTING STRUCTURES	KIRBY, KYLE K.
11145500	Not Issued	020	06/06/2005	SEMICONDUCTOR INTERCONNECT HAVING SEMICONDUCTOR SPRING CONTACTS, TEST SYSTEMS INCORPORATING THE INTERCONNECT AND TEST METHODS USING THE INTERCONNECT	KIRBY, KYLE K.
11140420	Not Issued	020	05/27/2005	METHODS AND APPARATUS FOR PLACING SUBSTRATES IN CONTACT WITH MOLTEN SOLDER	KIRBY, KYLE K.
11140402	Not Issued	020	05/27/2005	METHOD OF FORMING VIAS IN SEMICONDUCTOR SUBSTRATES WITHOUT DAMAGING ACTIVE REGIONS THEREOF AND RESULTING STRUCTURES	KIRBY, KYLE K.
11121276	Not Issued	020	05/03/2005	COMPLIANT CONTACT STRUCTURES, CONTACTOR	KIRBY, KYLE K.

				CARDS AND TEST SYSTEM INCLUDING SAME, AND METHODS OF FABRICATION AND USE	
11099066	Not Issued	020	04/05/2005	SYSTEMS AND METHODS OF PLATING VIA INTERCONNECTS	KIRBY, KYLE K.
11056211	Not Issued	030	02/10/2005	MICROELECTRONIC WORKPIECES AND METHODS FOR FORMING INTERCONNECTS IN MICROELECTRONIC WORKPIECES	KIRBY, KYLE K.
11044941	Not Issued	071	01/27/2005	SEMICONDUCTOR INTERCONNECT HAVING SEMICONDUCTOR SPRING CONTACTS	KIRBY, KYLE K.
11044443	Not Issued	030	01/26/2005	VIAS HAVING VARYING DIAMETERS AND FILLS FOR USE WITH A SEMICONDUCTOR DEVICE AND METHODS OF FORMING SEMICONDUCTOR DEVICE STRUCTURES INCLUDING SAME	KIRBY, KYLE K.
11031160	Not Issued	030	01/06/2005	SEMICONDUCTOR INTERCONNECT HAVING CONDUCTIVE SPRING CONTACTS	KIRBY, KYLE K.
11030772	Not Issued	030	01/06/2005	METHOD FOR FABRICATING A SEMICONDUCTOR INTERCONNECT HAVING CONDUCTIVE SPRING CONTACTS	KIRBY, KYLE K.
11028918	Not Issued	020	01/03/2005	METHODS OF FABRICATING INTERCONNECTS FOR SEMICONDUCTOR COMPONENTS	KIRBY, KYLE K.
11028892	Not Issued	030	01/03/2005	METHODS OF FABRICATING INTERCONNECTS FOR SEMICONDUCTOR COMPONENTS	KIRBY, KYLE K.
11027443	Not Issued	030	12/30/2004	METHODS FOR FORMING INTERCONNECTS IN MICROELECTRONIC WORKPIECES AND MICROELECTRONIC	KIRBY, KYLE K.

				WORKPIECES FORMED USING SUCH METHODS	
10999344	Not Issued	071	11/30/2004	METHODS OF FORMING VIA PLUGS USING AN AEROSOL STREAM OF PARTICLES TO DEPOSIT CONDUCTIVE MATERIAL	KIRBY, KYLE K.
10962931	Not Issued	030	10/11/2004	MULTI-DICE CHIP SCALE SEMICONDUCTOR COMPONENTS	KIRBY, KYLE K.
10932296	Not Issued	030	09/02/2004	THROUGH-WAFER INTERCONNECTS FOR PHOTOIMAGER AND MEMORY WAFERS	KIRBY, KYLE
10931959	Not Issued	030	08/31/2004	METHODS OF MANUFACTURE OF A VIA STRUCTURE COMPRISING A PLURALITY OF CONDUCTIVE ELEMENTS, SEMICONDUCTOR DIE, MULTICHIP MODULE, AND SYSTEM INCLUDING SAME	KIRBY, KYLE K.
10931847	Not Issued	030	08/31/2004	WAFER REINFORCEMENT STRUCTURE AND METHODS OF FABRICATION	KIRBY, KYLE K.
10928598	Not Issued	030	08/27/2004	METHODS FOR FORMING VIAS OF VARYING LATERAL DIMENSIONS AND SEMICONDUCTOR COMPONENTS AND ASSEMBLIES INCLUDING SAME	KIRBY, KYLE K.
10927486	Not Issued	030	08/26/2004	MICROFEATURE DIES WITH POROUS REGIONS, AND ASSOCIATED METHODS AND SYSTEMS	KIRBY, KYLE K.
10925501	Not Issued	020	08/24/2004	METHODS FOR FORMING INTERCONNECTS IN VIAS AND MICROELECTRONIC WORKPIECES INCLUDING SUCH INTERCONNECTS	KIRBY, KYLE K.
10925406	Not Issued	030	08/24/2004	MICROELECTRONIC IMAGERS WITH OPTICAL DEVICES HAVING INTEGRAL REFERENCE FEATURES AND METHODS FOR MANUFACTURING SUCH MICROELECTRONIC IMAGERS	KIRBY, KYLE K.

10919604	Not Issued	030	08/16/2004	FRAME STRUCTURE AND SEMICONDUCTOR ATTACH PROCESS FOR USE THEREWITH FOR FABRICATION OF IMAGE SENSOR PACKAGES AND THE LIKE, AND RESULTING PACKAGES	KIRBY, KYLE K.
10894262	Not Issued	030	07/19/2004	MICROELECTRONIC IMAGERS WITH OPTICAL DEVICES AND METHODS OF MANUFACTURING SUCH MICROELECTRONIC IMAGERS	KIRBY, KYLE K.
10879838	Not Issued	030	06/29/2004	MICROELECTRONIC DEVICES AND METHODS FOR FORMING INTERCONNECTS IN MICROELECTRONIC DEVICES	KIRBY, KYLE K.
10879398	Not Issued	030	06/29/2004	PACKAGED MICROELECTRONIC IMAGERS AND METHODS OF PACKAGING MICROELECTRONIC IMAGERS	KIRBY, KYLE K.
10871153	Not Issued	030	06/18/2004	COMPLIANT SPRING CONTACTS, METHODS OF MAKING, AND UTILIZATION AS ELECTRICAL CONTACTS IN PROBE CARD AND FLIP-CHIP APPLICATIONS	KIRBY, KYLE K.
10867352	Not Issued	071	06/14/2004	MICROELECTRONIC IMAGERS AND METHODS OF PACKAGING MICROELECTRONIC IMAGERS	KIRBY, KYLE K.
10864974	Not Issued	030	06/10/2004	PACKAGED MICROELECTRONIC IMAGERS AND METHODS OF PACKGING MICROELECTRONIC IMAGERS	KIRBY, KYLE K.
10860699	Not Issued	030	06/02/2004	SYSTEMS AND METHODS FOR TESTING MICROELECTRONIC IMAGERS AND MICROFEATURE DEVICES	KIRBY, KYLE K.
10804366	Not	030	03/19/2004	ETCH SOLUTION FOR	KIRBY, KYLE K.

	Issued			SELECTIVELY REMOVING SILICON AND METHODS OF SELECTIVELY REMOVING SILICON	
10791195	Not Issued	041	03/02/2004	COMPLIANT CONTACT PIN ASSEMBLY, CARD SYSTEM AND METHODS THEREOF	KIRBY, KYLE K.
10785466	Not Issued	030	02/23/2004	PACKAGED MICROELECTRONIC IMAGERS AND METHODS OF PACKAGING MICROELECTRONIC IMAGERS	KIRBY, KYLE K.
10784074	Not Issued	094	02/20/2004	METHODS OF FABRICATING INTERCONNECTS FOR SEMICONDUCTOR COMPONENTS	KIRBY, KYLE K.
10733226	Not Issued	020	12/10/2003	MICROELECTRONIC DEVICES AND METHODS FOR FILLING VIAS IN MICROELECTRONIC DEVICES	KIRBY, KYLE K.
10730333	Not Issued	030	12/08/2003	WAFER LEVEL METHODS FOR FABRICATING MULTI-DICE CHIP SCALE SEMICONDUCTOR COMPONENTS	KIRBY, KYLE K.
10713878	Not Issued	071	11/13/2003	MICROELECTRONIC DEVICES, METHODS FOR FORMING VIAS IN MICROELECTRONIC DEVICES, AND METHODS FOR PACKAGING MICROELECTRONIC DEVICES	KIRBY, KYLE K.
10690417	Not Issued	030	10/20/2003	METHODS OF COATING AND SINGULATING WAFERS AND CHIP-SCALE PACKAGES FORMED THEREFROM	KIRBY, KYLE K.
10684621	Not Issued	041	10/14/2003	COMPLIANT CONTACT STRUCTURES, CONTACTOR CARDS AND TEST SYSTEM INCLUDING SAME, AND METHODS OF FABRICATION AND USE	KIRBY, KYLE K.
10682703	Not Issued	094	10/09/2003	METHODS OF PLATING VIA INTERCONNECTS	KIRBY, KYLE K.
10673692	Not Issued	041	09/29/2003	METHOD FOR CREATING ELECTRICAL PATHWAYS FOR	KIRBY, KYLE K.

				SEMICONDUCTOR DEVICE STRUCTURES USING LASER MACHINING PROCESSES	
10666742	Not Issued	041	09/19/2003	METHODS FOR THINNING SEMICONDUCTOR SUBSTRATES THAT EMPLOY SUPPORT STRUCTURES FORMED ON THE SUBTRATES	KIRBY, KYLE K.
10619650	Not Issued	041	07/14/2003	SEMICONDUCTOR INTERCONNECT HAVIN COMPLIANT CONDUCTIVE CONTACTS	KIRBY, KYLE K.
10379949	Not Issued	071	03/06/2003	TEST SYSTEM AND TEST METHOD WITH INTERCONNECT HAVING SEMICONDUCTOR SPRING CONTACTS	KIRBY, KYLE K.
10370363	6819512	150	02/18/2003	TRIPLE-ATTRIBUTE METHOD OF ENCODING AND DECODING MAGNETIC DATA	KIRBY, KYLE K.
10161290	6641175	150	06/03/2002	RAMP-LOCK FITTING DEVICE	KIRBY, KYLE K.

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Application#	Patent#	Status	Date Filed	Title	Inventor Name 8
10919821	6922297	150	08/17/2004	DOUBLE-ATTRIBUTE METHOD OF ENCODING AND DECODING MAGNETIC DATA	KIRBY, KYLE KENDRICK
10653766	Not Issued	095	09/03/2003	PROBE CARD FOR USE WITH MICROELECTRONIC COMPONENTS,AND METHODS FOR MAKING SAME	KIRBY, KYLE K.
10613235	6855631	150	07/03/2003	METHODS OF FORMING VIA PLUGS USING AN AEROSOL STREAM OF PARTICLES TO DEPOSIT CONDUCTIVE MATERIAL	KIRBY, KYLE K.
10403937	6841883	150	03/31/2003	MULTI-DICE CHIP SCALE SEMICONDUCTOR COMPONENTS AND WAFER LEVEL METHODS OF FABRICATION	KIRBY, KYLE K.
09724469	6422607	150	11/28/2000	RAMP-LOCK FITTING DEVICE	KIRBY, KYLE K.
09630922	6299215	150	08/02/2000	COUPLING FITTING	KIRBY, KYLE K.
09088401	6250688	150	06/01/1998	RAMP-LOCK LIFTING DEVICE	KIRBY, KYLE K.

Inventor Search Completed: No Records to Display.

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Last Name = HIATT
First Name = WILLIAM

Application#	Patent#	Status	Date Filed	Title	Inventor Name 50
11146783	Not Issued	020	06/07/2005	PACKAGED MICROELECTRONIC IMAGERS AND METHODS OF PACKAGING MICROELECTRONIC IMAGERS	HIATT, WILLIAM M.
11133085	Not Issued	020	05/19/2005	BACKSIDE METHOD AND SYSTEM FOR FABRICATING SEMICONDUCTOR COMPONENTS WITH CONDUCTIVE INTERCONNECTS	HIATT, WILLIAM M.
10981023	Not Issued	040	11/04/2004	FOLDABLE BED WITH FOLDABLE GUARDRAIL	HIATT, WILLIAM CHARLES
10980554	Not Issued	020	11/03/2004	SYSTEM HAVING SEMICONDUCTOR COMPONENT WITH ENCAPSULATED, BONDED, INTERCONNECT CONTACTS	HIATT, WILLIAM M.
10962931	Not Issued	030	10/11/2004	MULTI-DICE CHIP SCALE SEMICONDUCTOR COMPONENTS	HIATT, WILLIAM M.
10934635	Not Issued	030	09/02/2004	SELECTIVE NICKEL PLATING OF ALUMINUM, COPPER, AND TUNGSTEN STRUCTURES	HIATT, WILLIAM M.
10931678	Not Issued	030	08/31/2004	METHODS FOR FABRICATION OF THIN SEMICONDUCTOR ASSEMBLIES INCLUDING REDISTRIBUTION LAYERS AND PACKAGES AND ASSEMBLIES FORMED THEREBY	HIATT, WILLIAM M.
10925796	Not Issued	030	08/24/2004	PASS THROUGH VIA TECHNOLOGY FOR USE DURING THE MANUFACTURE	HIATT, WILLIAM M.

				OF A SEMICONDUCTOR DEVICE	
10925501	Not Issued	020	08/24/2004	METHODS FOR FORMING INTERCONNECTS IN VIAS AND MICROELECTRONIC WORKPIECES INCLUDING SUCH INTERCONNECTS	HIATT, WILLIAM M.
10879838	Not Issued	030	06/29/2004	MICROELECTRONIC DEVICES AND METHODS FOR FORMING INTERCONNECTS IN MICROELECTRONIC DEVICES	HIATT, WILLIAM M.
10879398	Not Issued	030	06/29/2004	PACKAGED MICROELECTRONIC IMAGERS AND METHODS OF PACKAGING MICROELECTRONIC IMAGERS	HIATT, WILLIAM M.
10867505	Not Issued	030	06/14/2004	PREFABRICATED HOUSINGS FOR MICROELECTRONIC IMAGERS AND METHODS FOR PACKAGING MICROELECTRONIC IMAGERS	HIATT, WILLIAM M.
10867352	Not Issued	071	06/14/2004	MICROELECTRONIC IMAGERS AND METHODS OF PACKAGING MICROELECTRONIC IMAGERS	HIATT, WILLIAM M.
10864974	Not Issued	030	06/10/2004	PACKAGED MICROELECTRONIC IMAGERS AND METHODS OF PACKGING MICROELECTRONIC IMAGERS	HIATT, WILLIAM M.
10863994	Not Issued	030	06/09/2004	WAFER-LEVEL PACKAGED MICROELECTRONIC IMAGERS AND PROCESSES FOR WAFER-LEVEL PACKAGING	HIATT, WILLIAM M.
10860699	Not Issued	030	06/02/2004	SYSTEMS AND METHODS FOR TESTING MICROELECTRONIC IMAGERS AND MICROFEATURE DEVICES	HIATT, WILLIAM M.
10839457	Not Issued	030	05/05/2004	SYSTEMS AND METHODS FOR FORMING APERTURES IN	HIATT, WILLIAM M.

				MICROFEATURE WORKPIECES	
10801205	Not Issued	030	03/16/2004	STRESS AND FORCE MANAGEMENT TECHNIQUES FOR A SEMICONDUCTOR DIE	HIATT, WILLIAM M.
10791195	Not Issued	041	03/02/2004	COMPLIANT CONTACT PIN ASSEMBLY, CARD SYSTEM AND METHODS THEREOF	HIATT, WILLIAM M.
10788941	Not Issued	041	02/27/2004	ELECTRICAL CONTACTS, DEVICES INCLUDING THE SAME, AND ASSOCIATED METHODS OF FABRICATION	HIATT, WILLIAM M.
10784436	Not Issued	030	02/23/2004	THROUGH-HOLE CONDUCTORS FOR SEMICONDUCTOR SUBSTRATES AND METHOD AND SYSTEM FOR MAKING SAME	HIATT, WILLIAM M.
10767232	Not Issued	030	01/28/2004	MICROELECTRONIC DEVICES WITH IMPROVED HEAT DISSIPATION AND METHODS FOR COOLING MICROELECTRONIC DEVICES	HIATT, WILLIAM M.
10762433	Not Issued	092	01/22/2004	SYSTEM AND METHOD FOR THE TREATMENT OF SOOT-LADEN WATER	HIATT, WILLIAM C.
10755905	Not Issued	030	01/12/2004	METHODS OF FABRICATING SUBSTRATES AND SUBSTRATE PRECURSOR STRUCTURES RESULTING THEREFROM	HIATT, WILLIAM M.
10733226	Not Issued	020	12/10/2003	MICROELECTRONIC DEVICES AND METHODS FOR FILLING VIAS IN MICROELECTRONIC DEVICES	HIATT, WILLIAM M.
10730333	Not Issued	030	12/08/2003	WAFER LEVEL METHODS FOR FABRICATING MULTI-DICE CHIP SCALE SEMICONDUCTOR COMPONENTS	HIATT, WILLIAM M.
10723363	Not Issued	030	11/26/2003	PACKAGED MICROELECTRONIC IMAGERS AND METHODS OF PACKAGING MICROELECTRONIC	HIATT, WILLIAM M.

				IMAGERS	
10713626	Not Issued	030	11/13/2003	MICROELECTRONIC DEVICES HAVING CONDUCTIVE COMPLEMENTARY STRUCTURES AND METHODS OF MANUFACTURING MICROELECTRONIC DEVICES HAVING CONDUCTIVE COMPLEMENTARY STRUCTURES	HIATT, WILLIAM M.
10705729	Not Issued	030	11/10/2003	METHODS FOR REMOVING AND RECLAIMING UNCONSOLIDATED MATERIAL FROM SUBSTRATES FOLLOWING FABRICATION OF OBJECTS THEREON BY PROGRAMMED MATERIAL CONSOLIDATION TECHNIQUES	HIATT, WILLIAM M.
10705728	Not Issued	071	11/10/2003	METHODS FOR SUPPORTING SUBSTRATES DURING FABRICATION OF ONE OR MORE OBJECTS THEREON BY PROGRAMMABLE MATERIAL CONSOLIDATION TECHNIQUES	HIATT, WILLIAM M.
10705409	Not Issued	030	11/10/2003	SUBSTRATE SUPPORTS FOR USE WITH PROGRAMMABLE MATERIAL CONSOLIDATION APPARATUS AND SYSTEMS	HIATT, WILLIAM M.
10705405	Not Issued	020	11/10/2003	CLEANING COMPONENTS FOR USE WITH PROGRAMMABLE MATERIAL CONSOLIDATION APPARATUS AND SYSTEMS	HIATT, WILLIAM M.
10705250	Not Issued	061	11/10/2003	HANDLING SYSTEM FOR USE WITH PROGRAMMABLE MATERIAL CONSOLIDATION SYSTEMS AND ASSOCIATED METHODS	HIATT, WILLIAM M.
10690417	Not Issued	030	10/20/2003	METHODS OF COATING AND SINGULATING WAFERS AND CHIP-SCALE PACKAGES FORMED THEREFROM	HIATT, WILLIAM M.
10456585	Not Issued	030	06/09/2003	IMAGER WITH TUNED COLOR FILTER	HIATT, WILLIAM M.
10403937	6841883	150	03/31/2003	MULTI-DICE CHIP SCALE	HIATT, WILLIAM

				SEMICONDUCTOR COMPONENTS AND WAFER LEVEL METHODS OF FABRICATION	M.
10390863	6906418	150	03/17/2003	SEMICONDUCTOR COMPONENT HAVING ENCAPSULATED, BONDED, INTERCONNECT CONTACTS	HIATT, WILLIAM M.
10228906	6710442	150	08/27/2002	MICROELECTRONIC DEVICES WITH IMPROVED HEAT DISSIPATION AND METHODS FOR COOLING MICROELECTRONIC DEVICES	HIATT, WILLIAM M.
10193567	6803303	150	07/11/2002	METHOD OF FABRICATING SEMICONDUCTOR COMPONENT HAVING ENCAPSULATED, BONDED, INTERCONNECT CONTACTS	HIATT, WILLIAM M.
10039829	6733672	150	10/19/2001	SYSTEM AND METHOD FOR THE TREATMENT OF SOOT-LADEN WATER	HIATT, WILLIAM C.
09661872	6426004	150	09/14/2000	CONTINUOUS FLOW COMPLETELY MIXED WASTE WATER TREATMENT REACTOR METHOD	HIATT, WILLIAM C.
08959179	5785863	150	10/28/1997	REMOVING METALS FROM AQUEOUS WASTE STREAMS	HIATT, WILLIAM C.
08130044	5401297	150	09/30/1993	PROCESS FOR REMOVING CAPROLACTAM EMISSIONS BY A WASHING METHOD	HIATT, WILLIAM C.
08130043	5409513	250	09/30/1993	APPARATUS FOR REMOVING CAPROLACTAM EMISSIONS	HIATT, WILLIAM C.
07554196	5177307	150	07/17/1990	COMPOSITIONS AND METHODS FOR MODULATION OF ENDOGENOUS CYTOKININ LEVELS	HIATT, WILLIAM
07382802	Not Issued	168	07/19/1989	COMPOSITIONS AND METHODS FOR MODULATION OF ENDOGENOUS CYTOKININ LEVELS	HIATT, WILLIAM
07381461	Not Issued	169	07/18/1989	COMPOSITIONS AND METHODS FOR MODULATION OF ENDOGENOUS CYTOKININ LEVELS	HIATT, WILLIAM
06395367	Not	161	07/06/1982	CENTRIFUGAL APPARATUS	HIATT, WILLIAM

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